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| 迭代 | 指令 | 发射 | 执行 | 存储器访问 | 写CDB | 注释 |
| 1 | L.D F2,0(R1) | 1 | 2 | 2 | 3 |  |
| 1 | MUL.D F4,F2,F0 | 2 | 4 | 4 | 19 | 等待F2的写回 |
| 1 | L.D F6,0(R2) | 3 | 4 | 4 | 5 |  |
| 1 | ADD.D F6,F4,F6 | 4 | 20 | 20 | 30 | 等待F6从乘法指令得到结果 |
| 1 | S.D F6,0(R2) | 5 | 31 | 31 |  | 等待F6从加法指令得到结果 |
| 1 | DADDIU R1,R1,#8 | 6 | 7 | 7 | 8 |  |
| 1 | DADDIU R2,R2,#8 | 7 | 8 | 8 | 9 |  |
| 1 | DSLTU R3,R1,R4 | 8 | 9 | 9 | 10 |  |
| 1 | BNEZ R3,foo | 9 | 11 |  |  | 等待DSLTU写回R3 |
| 2 | L.D F2,0(R1) | 10 | 12 | 12 | 13 | 等待BNEZ执行结束判断是否需要继续循环 |
| 2 | MUL.D F4,F2,F0 | 11 | 14 | 14 | 29 | 等待F2的写回 |
| 2 | L.D F6,0(R2) | 12 | 32 | 32 | 33 | 等待上一循环的加法指令执行结束且整数类型不再忙碌 |
| 2 | ADD.D F6,F4,F6 | 13 | 34 | 34 | 44 | 等待LD指令F6的写回 |
| 2 | S.D F6,0(R2) | 14 | 45 | 45 |  | 等待ADD.D指令写回F6 |
| 2 | DADDIU R1,R1,#8 | 15 | 16 | 16 | 17 |  |
| 2 | DADDIU R2,R2,#8 | 16 | 17 | 17 | 18 |  |
| 2 | DSLTU R3,R1,R4 | 17 | 18 | 18 | 19 |  |
| 2 | BNEZ R3,foo | 18 | 20 |  |  | 等待DSLTU写回R3 |
| 3 | L.D F2,0(R1) | 19 | 21 | 21 | 22 | 等待BNEZ执行结束 |
| 3 | MUL.D F4,F2,F0 | 20 | 23 | 23 | 38 | 等待F2 |
| 3 | L.D F6,0(R2) | 21 | 46 | 46 | 47 | 等待上一循环的ADD.D执行结束且整数类型指令不忙碌 |
| 3 | ADD.D F6,F4,F6 | 22 | 48 | 48 | 58 | 等待F6 |
| 3 | S.D F6,0(R2) | 23 | 59 | 59 |  | 等待F6 |
| 3 | DADDIU R1,R1,#8 | 24 | 25 | 25 | 26 |  |
| 3 | DADDIU R2,R2,#8 | 25 | 26 | 26 | 27 |  |
| 3 | DSLTU R3,R1,R4 | 26 | 27 | 27 | 28 |  |
| 3 | BNEZ R3,foo | 27 | 29 |  |  | 等待DSLTU的执行结果 |